

Ref #	Hits	Search Query	DBs	Default Operator	Plurals	Time Stamp
L1	1	("6201428").PN.	USPAT	OR	OFF	2005/08/26 15:37
L2	1240	(tri-state or tristate) and control and ((level adj1 shift\$4) or (level adj1 translat\$4) or (level adj1 conver\$4)) and (driver or buffer or (output adj1 stage))	US-PGPUB; USPAT; USOCR; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2005/08/26 17:55
L3	28	2 and ((low NEAR1 threshold NEAR1 transistor) or (native NEAR2 transistor))	US-PGPUB; USPAT; USOCR; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2005/08/26 16:07
L4	27	2 and native	US-PGPUB; USPAT; USOCR; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2005/08/26 16:07
L5	4	(tri-state or tristate) and ((level adj1 shift\$4) or (level adj1 translat\$4) or (level adj1 conver\$4)) and (native NEAR1 transistor)	US-PGPUB; USPAT; USOCR; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2005/08/26 16:19
L6	250	(native NEAR1 transistor)	US-PGPUB; USPAT; USOCR; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2005/08/26 16:19
L7	7	6 and (tri-state or tristate)	US-PGPUB; USPAT; USOCR; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2005/08/26 17:52
L8	237	"327"/\$.ccls. and native	US-PGPUB; USPAT; USOCR; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2005/08/26 17:49
L9	32	8 and (tri-state or tristate or (high adj1 impedance))	US-PGPUB; USPAT; USOCR; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2005/08/26 17:55

L10	24	(tri-state or tristate or (high adj1 impedance)) and control and ((level adj1 shift\$4) or (level adj1 translat\$4) or (level adj1 conver\$4)) and ((native NEAR2 transistor\$1) or w-fet or (w-channel NEAR1 transistor\$1))	US-PGPUB; USPAT; USOCR; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2005/08/26 17:58
-----	----	---	---	----	----	------------------